

FIG. 2

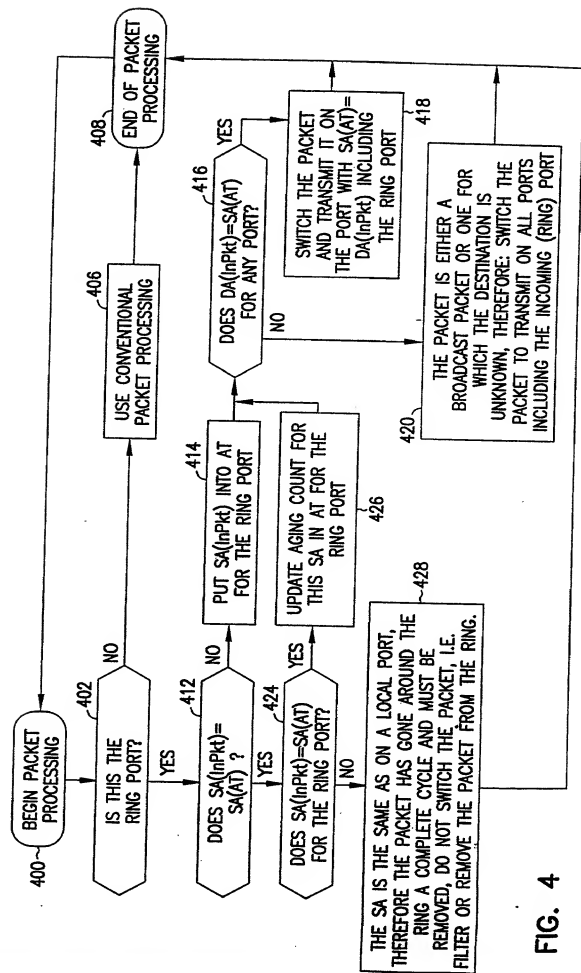


FIG. 4

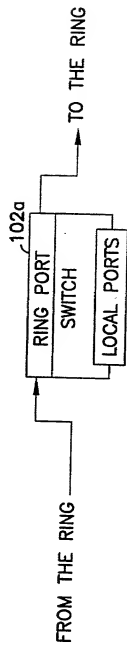


FIG. 3

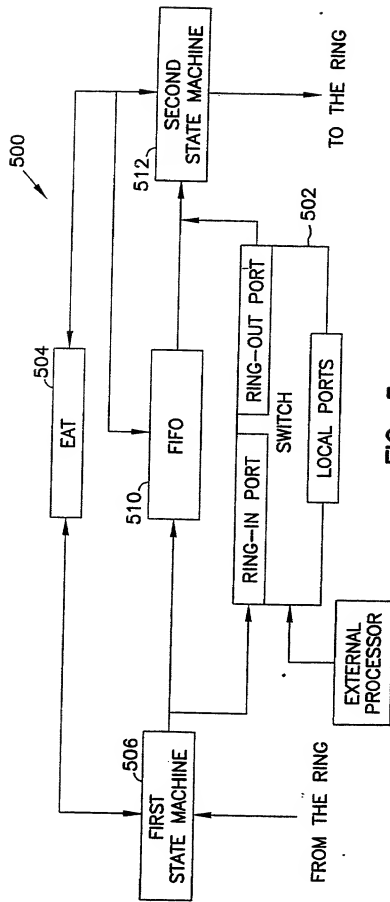


FIG. 5

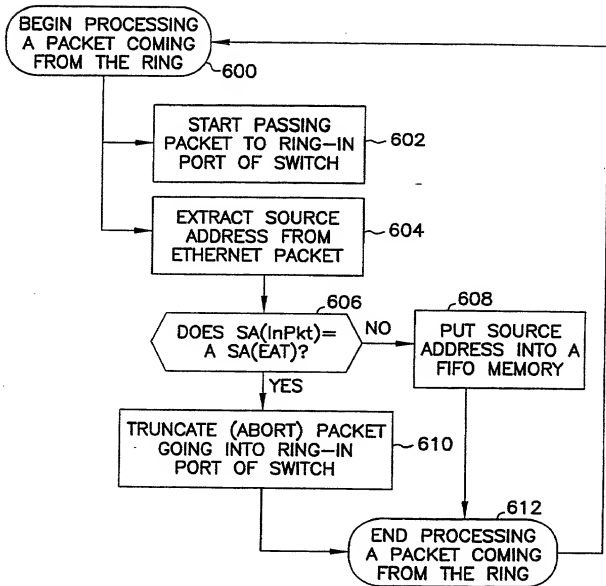


FIG. 6



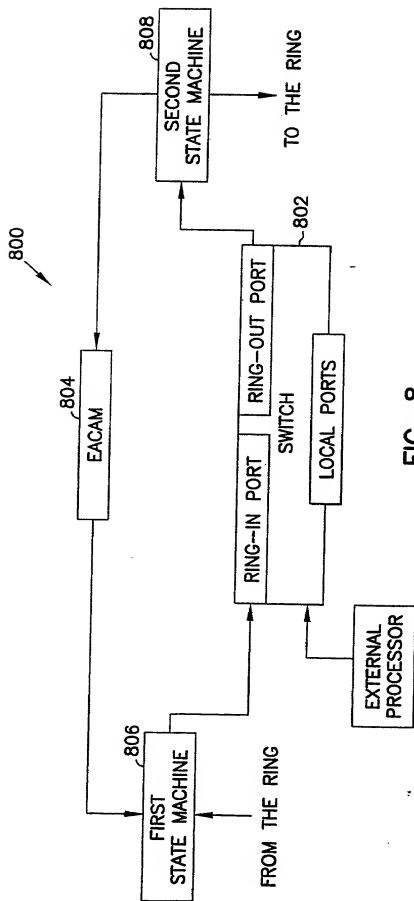


FIG. 8

```

graph TD
    900([BEGIN PROCESSING  
A PACKET COMING  
FROM THE RING]) --> 902[START PASSING  
PACKET TO RING-IN  
PORT OF SWITCH]
    900 --> 904[EXTRACT SOURCE  
ADDRESS FROM  
ETHERNET PACKET]
    902 --> 904
    904 --> 906{{906  
DOES SA(InPkt)=  
SA(EACAM)?}}
    906 -- NO --> 908[908  
PUT SOURCE  
ADDRESS INTO  
EACAM WITH  
LOCAL BIT=0]
    906 -- YES --> 912{{912  
DOES THE LOCAL BIT  
FOR SA(EACAM)=1?}}
    912 -- NO --> 908
    912 -- YES --> 914[914  
TRUNCATE (ABORT) PACKET  
GOING INTO RING-IN  
PORT OF SWITCH]
    908 --> 910([910  
END PROCESSING  
A PACKET COMING  
FROM THE RING])
    914 --> 910
  
```

FIG. 9



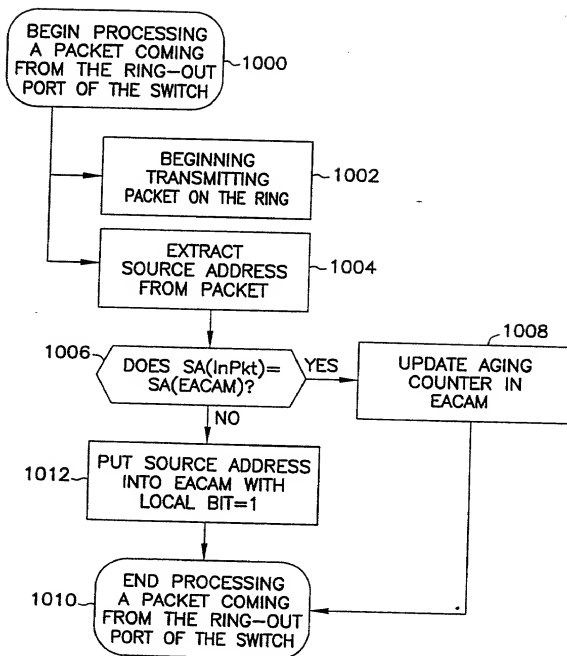


FIG. 10

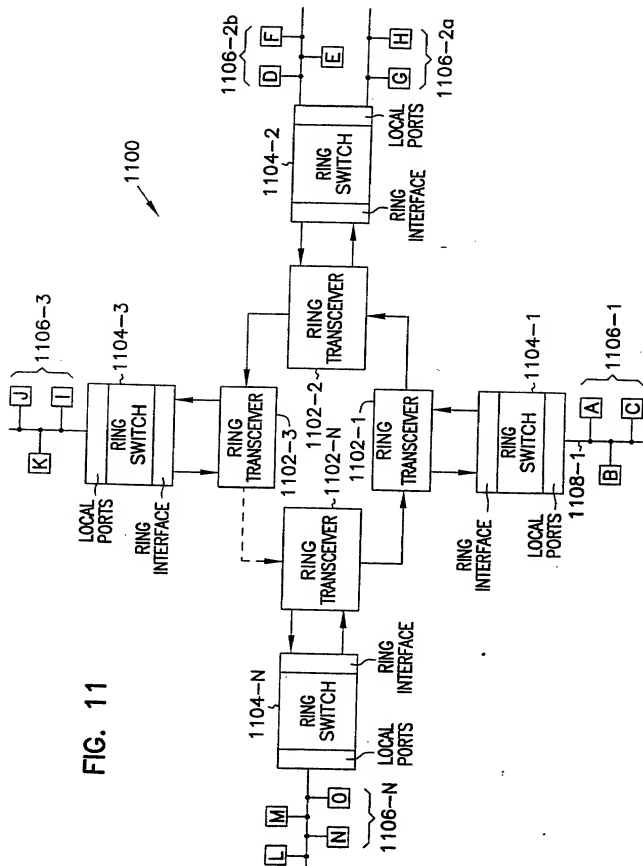


FIG. 12

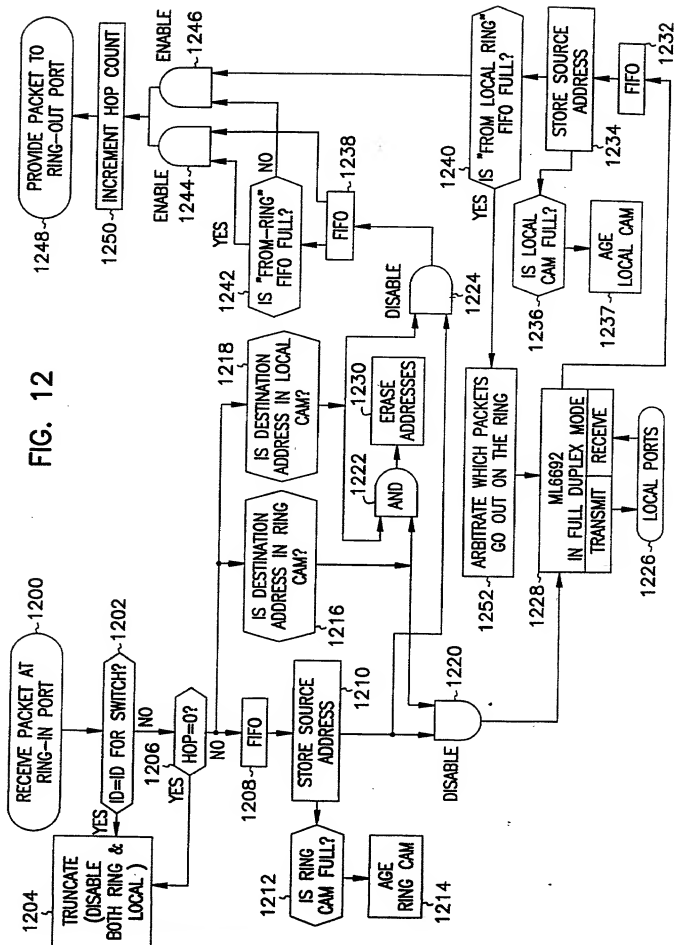


FIG. 13

